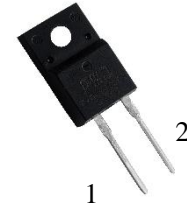


## SiC SBD P4D06010F2 650V SiC Schottky Diode

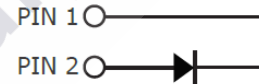


TO-220F-2

Cathode	1
Anode	2

### Features

- Qualified to AEC-Q101
- Ultra-Fast Switching
- Zero Reverse Recovery Current
- High-Frequency Operation
- Low  $V_F$  SBD series
- Positive Temperature Coefficient on  $V_F$
- High Surge Current
- Full Isolated Package for Direct Heat Sinking
- 100% UIS Tested



### Standards Benefits

- Improve System Efficiency
- Reduction of Heat Sink Requirement
- Essentially No Switching Losses
- Parallel Devices Without Thermal Runaway



### Application

- Consumer SMPS
- Boost Diodes in PFC or DC/DC Stages
- AC/DC Converters



### Order Information

Part Number	Package	Marking
P4D06010F2	TO-220F-2	P4D06010F2



## Contents

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## 1. Maximum Ratings

At  $T_J = 25^\circ\text{C}$ , unless specified otherwise

Parameter	Symbol	Value	Unit	Test condition
Repetitive Peak Reverse Voltage	$V_{RRM}$	650	V	$T_C = 25^\circ\text{C}$
Surge Peak Reverse Voltage	$V_{RSM}$	650	V	$T_C = 25^\circ\text{C}$
DC Blocking Voltage	$V_R$	650	V	$T_C = 25^\circ\text{C}$
Forward Current	$I_F$	20 10	A	$T_C = 25^\circ\text{C}$ $T_C = 110^\circ\text{C}$
Non-Repetitive Forward Surge Current	$I_{FSM}$	80 55	A	$T_C = 25^\circ\text{C}, t_p = 10\text{ms}$ $T_C = 125^\circ\text{C}, t_p = 10\text{ms}$
Repetitive Peak Forward Surge Current	$I_{FRM}$	40 18	A	$T_C = 25^\circ\text{C}, t_p = 10\text{ms}$ $T_C = 125^\circ\text{C}, t_p = 10\text{ms}$
Power Dissipation	$P_{tot}$	35	W	$T_C = 25^\circ\text{C}$
Operating Junction and Storage Temperature	$T_J, T_{STG}$	-55 to +175	$^\circ\text{C}$	
TO-220 Mounting Torque M3 Screw	$T_{orq}$	1 8.8	N*m lbf-in	

## 2. Electrical Characteristics

At  $T_J = 25^\circ\text{C}$ , unless specified otherwise

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Forward Voltage	$V_F$	/	1.29	1.5	V	$I_F = 10\text{A}, T_J = 25^\circ\text{C}$
			1.45	/		$I_F = 10\text{A}, T_J = 175^\circ\text{C}$
Reverse Current	$I_R$	/	14	41	$\mu\text{A}$	$V_R = 650\text{V}, T_J = 25^\circ\text{C}$
			200	/		$V_R = 650\text{V}, T_J = 175^\circ\text{C}$
Total Capacitance	C	/	555	/	pF	$V_R = 0\text{V}, T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
			59			$V_R = 200\text{V}, T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
			44			$V_R = 400\text{V}, T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
Total Capacitive Charge	$Q_C$	/	26	/	nC	$V_R = 400\text{V}, I_F = 10\text{A}$ $di/dt = 500\text{A}/\mu\text{s}$ $T_J = 25^\circ\text{C}$

## 3. Thermal Characteristics

Parameter	Symbol	Values	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	4.3	$^\circ\text{C}/\text{W}$

## 4. Typical Performance

At  $T_J = 25^\circ\text{C}$ , unless specified otherwise

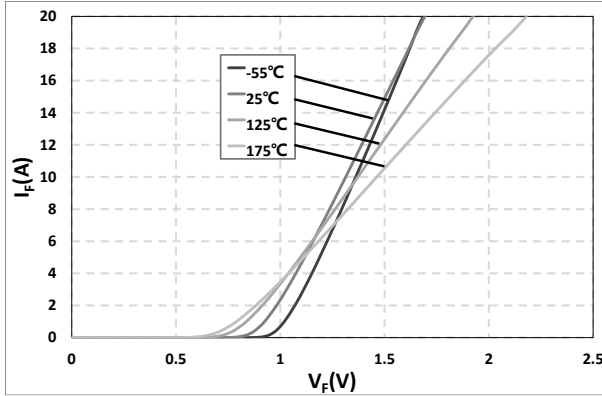


Fig. 1 Typical Forward Characteristics  
 $I_F = f(V_F)$ ;  $T_J = -55^\circ\text{C}, 25^\circ\text{C}, 125^\circ\text{C}, 175^\circ\text{C}$

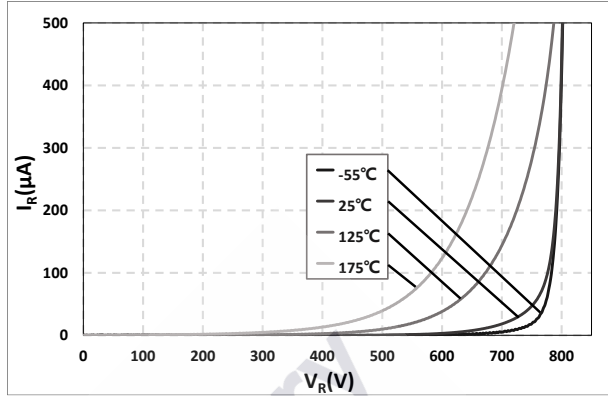


Fig. 2 Reverse Characteristics  
 $I_R = f(V_R)$ ;  $T_J = -55^\circ\text{C}, 25^\circ\text{C}, 125^\circ\text{C}, 175^\circ\text{C}$

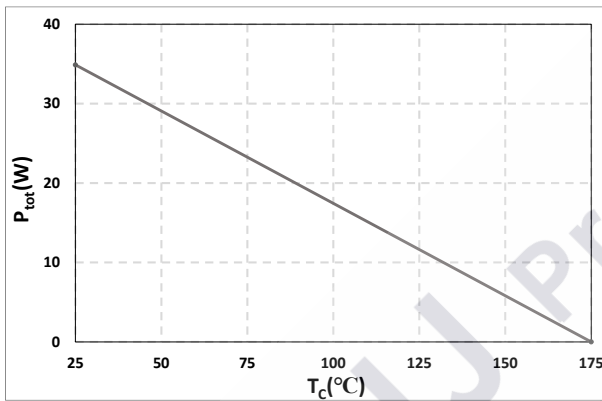


Fig. 3 Typical Power Derating  
 $P_{tot} = f(T_c)$

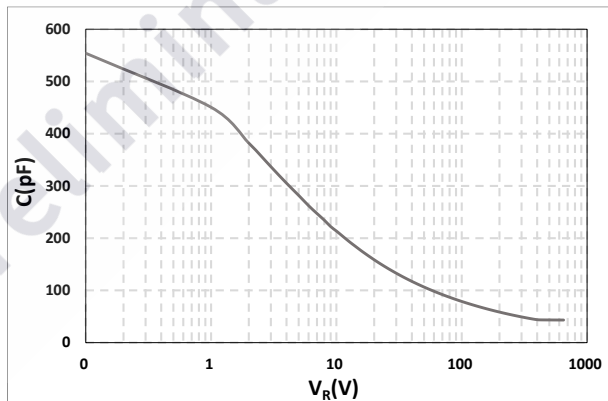


Fig. 4 Typical Total Capacitance  
 $C = f(V_R)$

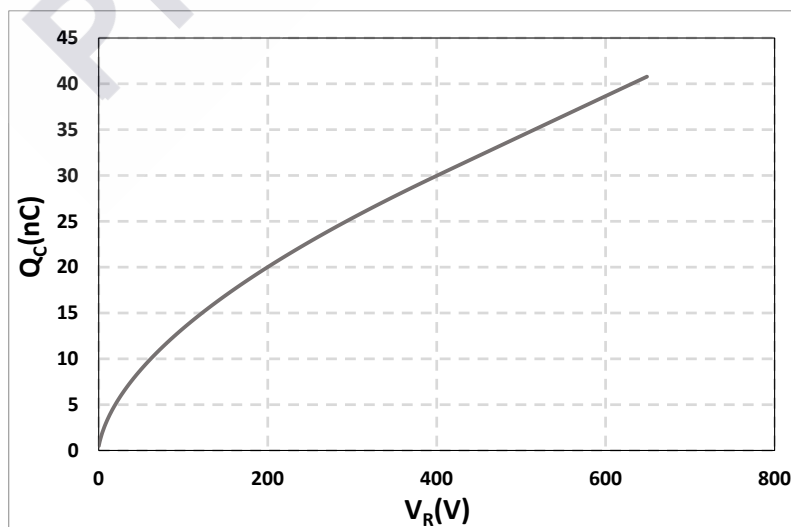
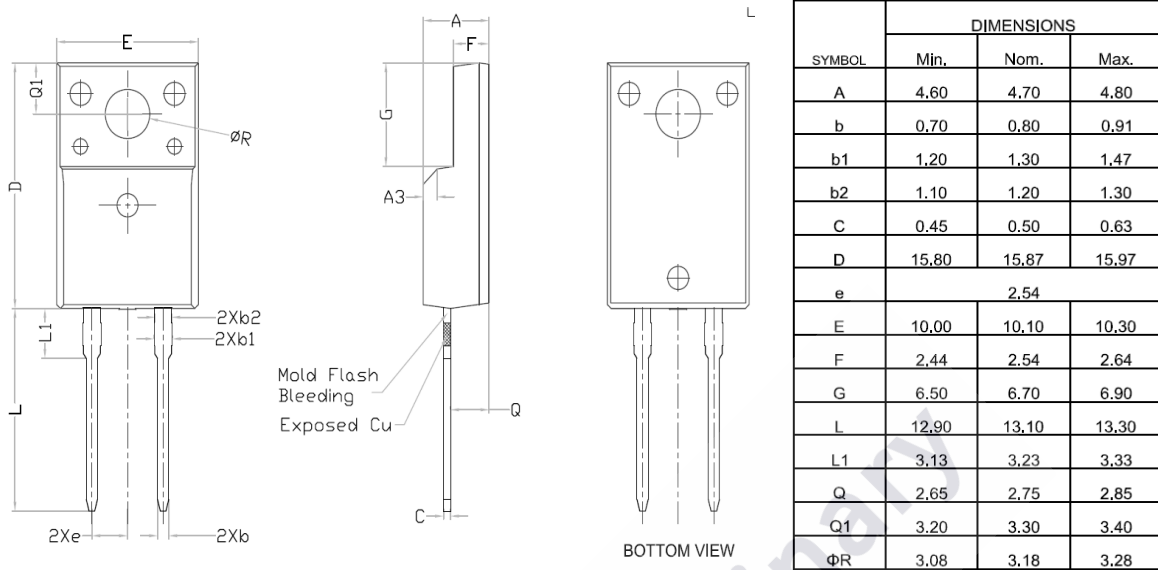


Fig. 5 Typical Total Capacitive Charge  
 $Q_C = f(V_R)$

### 5. Package Outlines



Drawing and dimensions

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